

L Number	Hits	Search Text	DB	Time stamp
6	19	"5649165"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 15:32
7	13	((binary near2 decision near2 daigram) or BDD) and "5649165"	USPAT; EPO; JPO; DERWENT	2003/10/14 15:34
8	12	(replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and ((binary near2 decision near2 daigram) or BDD) and "5649165")	USPAT; EPO; JPO; DERWENT	2003/10/14 15:50
9	12	function and ((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and ((binary near2 decision near2 daigram) or BDD) and "5649165"))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:12
12	5	(set near2 function) and ((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and ((binary near2 decision near2 daigram) or BDD) and "5649165"))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:28
15	239088	((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:27
16	748	((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:41
17	61	(((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:41
18	35	(replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and ((((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:35
19	11	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:40

20	11	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:42
21	889	((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:41
22	85	(((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:41
23	18	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:42